

In the Specification:

Please amend the specification as follows:

Page 2, first full paragraph, lines 3-20 as follows:

A1
"One problem with crystals is that, under certain conditions, the frequency of their signals tend tends to drift away from the expected frequency. The phase of the crystal signal is still stable; however, the frequency may now be different. Temperature, age of the crystal, and other known factors can cause this unfortunate phenomenon. When this occurs, given that the high frequency signal generated by the frequency synthesizer is a multiple of the crystal frequency, the high frequency signal drifts as well. This can lead to poor transmission and/or reception between radios as the frequency being used is no longer the desired frequency. To remedy this problem, periodic calibration of radios is performed. This entails adjusting the crystal frequency to obtain the correct high frequency signal. Clearly, such methods can be expensive, requiring time consuming disassembly of radios and shop time for technicians to perform the calibration."

Page 6, last paragraph, line 30 to page 7 line 5 as follows:

A2
"Once the true input frequency is known, the controller can thereby calculate the multiplier value required by the frequency source 20 to produce a signal with an output frequency 50 approximately equal to a desired frequency 60. Once this corrected multiplier value is found by the controller 10 by dividing the desired frequency to the true input frequency, the corrected multiplier is transmitted to the frequency source 20 so that it can be used to produce the desired frequency."

Page 8, first full paragraph, lines 7-8, as follows:

A3
"A logic flow diagram followed by the controller as described above 10 is illustrated in Figure 3."